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## APPLICANTS

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**\*\* CONTINUING DATA \*\*\*\*\***

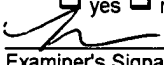
This application is a DIV of 10/118,039 04/09/2002 PAT 6,787,844  
 which is a DIV of 09/706,810 11/07/2000 PAT 6,525,370  
 which is a DIV of 09/317,255 05/24/1999 ABN  
 which is a DIV of 08/720,014 09/27/1996 PAT 5,925,907

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 07-276292 09/29/1995

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

**\*\* 11/22/2003**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance  Examiner's Signature Initials	STATE OR  COUNTRY JAPAN	SHEETS  DRAWING 4	TOTAL  CLAIMS 15	INDEPENDENT  CLAIMS 2
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## TITLE

Semiconductor device including transistor with composite gate structure and transistor with single gate structure,  
 and method for manufacturing the same

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